

**Electromagnetic Simulation Software** 

## Impedance Tuner Matching

Overview of XFdtd's schematic editor

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## **XFdtd Full-Wave Simulation**





### Cellular Phone

- LTE antennas w/ carrier aggregation
- Diversity, WiFi, BlueTooth, GPS antennas
- 5G FR2 in mmWave bands
- Compliance testing for human exposure (SAR)

### Automotive Radar

- 24-81 GHz bands
- Antenna, feeding network, LO design and coupling
- Radome, mounting bracket, fascia attenuation analysis

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# Optimizations in the Schematic Editor Bands & Standards



### Additional Bands + Modern Industrial Designs + Device States = Necessitates the need for tuners

\*Motorola Mobility provided the 3-D CAD model of the phone, which was then modified for demonstration purposes to include an external floating antenna

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[1] https://www.rfpage.com/what-are-5g-frequency-bands/

## What's New in XFdtd?

- Incorporated particle swarm optimizer directly into our schematic editor allowing us to optimize fixed component types and values as well as the tune codes associated with specific goals and operating modes.
- The optimizer can optimize on a variety of antenna/RF metrics such as return loss, isolation between ports, and system efficiency.



## **Example Project Summary**

- Simple DILA Antenna Structure
- Employ impedance and aperture tuners implemented as subcircuits in the overall matching circuit for the antenna
- Using new optimizations we'll get a matching circuit allowing for different cellular bands of operation
- The operating modes in our example will be different cellular frequency bands



## Workflow for Matching Network of DILA Antenna



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## **Device and Antenna with Tuners**





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### **Aperture Tuner Subcircuit**



**Aperture Tuner Subcircuit** 

Lower Level of the Subcircuit Containing the Underlying 3 Port S-Parameter Data

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## **Step 1** Layout



## Step 1 Layout in XFdtd





**Aperture Tuner Layout** 



Impedance Tuner Layout

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# Step 1 Matching Network Layout Details and Antenna

### **Aperture Tuner Layout**



Impedance Tuner Layout





Pads and transmission

lines connect all matching network

components

## **Step 2 XFdtd Simulation**



## **Step 2 Port Details of the Impedance Tuner Layout**





Port Details of the Aperture Tuner Layout

Port Details of the Impedance Tuner Layout

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## **Step 3** Schematic Editor



## **Step 3 Schematic Editor**

**Schematic Includes:** 

- 25-port FDTD Simulation
- Impedance Tuner
- Aperture Tuner
- 7 Fixed L or C Components





## **Step 4 Schematic Editor Optimizer**



## **Step 4 Define Operating Modes**

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$\stackrel{\pi}{\uparrow} \stackrel{\pi}{\uparrow}$ XFdtd - Edit Operating Mode List		×				
Default Mode Name: Default						
	Search: 🔹	•				
Operating Mode	<ul> <li>Properties</li> </ul>	Notes	<sup>∓</sup> <sup>#</sup> ∓ XFdtd - Edit Operating Mode			
Band 5 Schematic Editor Optimizer	Contains 8 property specifications		Name: Band 5 Schematic Editor Opt	timizer		
Band 48 Schematic Editor Optimizer	Contains 8 property specifications		E Component Properties			
Band 2 Schematic Editor Optimizer	Contains 8 property specifications		Add 📔 Delete	Import Export		
✓ Band 13 Schematic Editor Optimizer	Contains 8 property specifications		Component Generic Tuner Beta	Property C1	Value 1.0 pF	Description
			Generic Tuner Beta	C2	3.0 pF	
			Generic Tuner Beta	SW1	ON	
	ОК	Cancel	Generic Tuner Beta	SW2	ON	
			Generic Tuner Beta	SW3	ON	
			Generic Tuner Beta	SW4	ON	
			Generic Tuner Beta	SW5	ON	1

Generic Aperture Tuner Switch

Notes

Aperture\_Tuner\_Switch

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OK

•

Cancel

SW1\_OFF\_SSW1\_OPEN\_SW2\_OFF\_SSW2\_OPEN

 $\times$ 

## **Step 4 Configure Goals on Operating Modes**

	Goals						×	T T AF	ata - Design Goals				×
								+		Data Detail *	w Only Enabled Sear	ch: •	۲
+				Data Detail 🔹	Show Only Enab	oled Searc	h: •	Bar	Name         Result         Evaluation           d S UL         Band S         c c c < 6 dRo	Target Bounds Samples	Method	Weight	Goal 1
Name 🔹	Result	Evaluation	Target Bounds	Samples	Method	Weight	Goal Report Pass/Fail	1	tem  Schematic Edit  2_3 > 0 upp	[[-7 ddp, -5 ddp] [[0 874 GHz 0	R4 Center Weighted		) / J ( 0.00 ·
Band 5 UL Bystem	Band 5 Schematic Edit	ε_s > -6 dBp	[-7 dBp, -5 dBp]	5 samples in [0.824 GHz, 0.84	Center Weighted 1		0 / 5 ( 0.00 %)		Name: Band 5 UL System Efficiency			V E	nable Goal
Band 5 DL Bystem	Band 5 Schematic Edit	ε_s > -6 dBp	[-7 dBp, -5 dBp]	5 samples in [0.869 GHz, 0.89	Center Weighted 1		0 / 5 ( 0.00 %)		Operating Mode: Band 5 Schematic Editor C	iptimizer 👻			
Band 48 System Efficiency	Band 48 Schematic Edit	ε_s > -3 dBp	[-4 dBp, -2 dBp]	10 samples in [3.55 GHz, 3.7	Center Weighted 1		6 / 4 ( 60.00 %)	_	Sensor: XF 1 Result: System Efficiency	• •	_		
Band 2 UL Bystem	Band 2 Schematic Edit	ε_s > -3 dBp	[-4 dBp, -2 dBp]	5 samples in [1.85 GHz, 1.91 GHz]	Center Weighted 1		0 / 5 ( 0.00 %)		Part: Not Complex	•			
Band 2 DL Bystem	Band 2 Schematic Edit	ε_s > -3 dBp	[-4 dBp, -2 dBp]	5 samples in [1.93 GHz, 1.99 GHz]	Center Weighted 1		0 / 5 ( 0.00 %)						
Band 13 UL System	Band 13 Schematic Edit	ε_s > -6 dBp	[-7 dBp, -5 dBp]	5 samples in [0.777 GHz, 0.78	Center Weighted 1		3 / 2 ( 60.00 %)						
Band 13 DL Bystem	Band 13 Schematic Edit	$\epsilon_s > -6 \text{ dBp}$	[-7 dBp, -5 dBp]	5 samples in [0.746 GHz, 0.75	Center Weighted 1		2 / 3 ( 40.00 %)						
							OK Cancel	N	ame: Band 5 UL System Efficiency				
								Quantity	Weight: 1	Target	Samp	ling	
								5	Method: Center Weighted 💌	Target Type: Value		Source: Freque	ency Band 🔻
								evaluati	Comparison: > (Greater Than) *	Target: -6 dBp	<i>,</i>	Ø 🗋 🖿	1
										Min Bound: -7 dBp		Application	Legacy
										Max Bound: -5 dBp		Category	Response Mat
												Band Name	IS BS,CDMA BC
												Link Type	: Uplink
												Link Type Min	Uplink
												Link Type Min Max	<ul> <li>Uplink</li> <li>0.824 GHz</li> <li>0.849 GHz</li> </ul>

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Cancel

OK

## **Step 4 Configure Particle Swarm Optimizer**

A	
Name: Generic Tuner	Beta Generic Aperture Tuner
Workspace Solver Sett	ings Results
Frequency Sweep Settings	
Start Froquency	
Start Frequency.	
Stop Frequency:	5 GHz
Number of Points:	7001
Sweep Type:	Linear
Optimization Analysis Settin	ngs
<ul> <li>Specify number of</li> <li>Number of Particle</li> <li>Particles Per Variable</li> </ul>	particles per variable as: 100 le: 10
Max Iteration	<b>is:</b> 50000
	Advanced *
	Neighborhood Size: 5
	Number of Predators: 10
	CVT
	CVT

Details of the Particle Swarm Optimizer

### **PSO Execution**

② XFdtd - Scher	natic Optimizatior	1	
✓ Automatically	apply values		
Start	Stop		
Optimization Pr	ogress:		
Best F	itness: 0.871756	(0.00 %)	
Goals Eva	luated: 7/7		
Goals I	Passed:		
Name 💌	Success	Passed	
Band 13 DL System	<mark>4</mark> 0.00%	2	3
Band 13 UL System	60.00 <mark>%</mark>	3	2
Band 2 DL System	0.00%	0	5
Band 2 UL System	0.00%	0	5
Band 48 System Efficiency	60.00 <mark>%</mark>	6	4
Band 5 DL System	0.00%	0	5
Band 5 UL System	0.00%	0	5

### Optimizer output

Checking input schematic validity... Extracting properties for analysis... Preloading schematic data... Performing initial solve... Initializing particle swarm... Swarm initialized with 980 particles and 10000 generations... Optimizing... Finished generation 1 / 10000 fitness: 0.871756 Finished generation 2 / 10000 fitness: 0.871756 Finished generation 3 / 10000 fitness: 0.871756

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## **Step 5 Review Results**



## **Step 5 Optimized System Efficiency Results**



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## **Step 5 Tune Codes and Fixed Component Values**

Show All Properties V	Fill Def	ault Values											
Operating Mode	Tuner	B 1c Tuner	Be ric Tuner	Bet Tuner	Be Tuner	Generic Aperture Tuner Switch .Aperture_Tuner_Switch	Tuner	Be Tuner	Be ta.Regi	s ta.Regi	st eta.Regist	te Generic Tuner Beta. Tune Code (dependent)	Generic Aperture Tuner Switch .Register Aperture Tuner Switch (dependent
and 5 Schematic Editor Optimizer	ON	3.0 pF	1.4 pF	ON	ON	SW1_OFF_SSW1_OPEN_SW2_OFF_SSW2_OPEN	ON	ON	0x1f	0x0d	0x11	1f 0d 11	0x05
and 48 Schematic Editor Optimizer	ON	3.0 pF	3.0 pF	ON	ON	SW1_OFF_SSW1_OPEN_SW2_OFF_SSW2_OPEN	ON	OFF	0x1e	0x11	0x11	ie 11 11	0x05
and 13 Schematic Editor ptimizer	ON	3.0 pF	3.0 pF	ON	OFF	SW1_OFF_SSW1_SHORT_SW2_OFF_SSW2_SHORT	ON	ON	0x1d	0x11	0x11	1d 11 11	0x0A
efault (Default)	ON	0.1 pF	3.0 pF	OFF	OFF	SW1_ON_SSW1_OPEN_SW2_OFF_SSW2_OPEN	OFF	OFF	0x10	0x11	0x00	10 11 00	0x0D
and 2 Schematic Editor Optimizer	ON	3.0 pF	3.0 pF	OFF	ON	SW1_ON_SSW1_OPEN_SW2_ON_SSW2_OPEN	ON	ON	0x17	0x11	0x11	17 11 11	0x0F

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## **Push Schematic to XF Simulation**

- As long as the simulation is set up properly, we only have to run one XF simulation.
- We can push the schematic back onto our previously run XF simulation to determine quantities such as envelope correlation coefficients, far field patterns, near field results such as SAR.
- By use of the superposition principle these data can be determined from the previously run XF simulation.
- Allows users to quickly post process the full-wave results in the presence of the circuit.



## Contact



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## **Backup Slides**



## LC Definitions

### Allowable component values

10 M						Show Only Enabled     Search:
Component	▼ Property	Current	Enabled	Continuity	Analyze	Values
			false		Auto	All values (Enabled, Disabled (Open))
Z_Port_3	Capacitance	44 pF	true	Discrete	Custom	140 values ([ 0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
Z_Port_3	RealInductance	4 nH	true	Discrete	Custom	150 values ([ 0.1 nH : 10 nH : 0.1 nH], [11 nH : 60 nH : 1 nH])
Z_Port_3	Selector	RealL	true	Discrete	Auto	All values (C, RealL)
			false		Auto	
			false			All values (Enabled, Disabled (Open))
Z_Port_4			false			All values (Enabled, Disabled (Short), Disabled (Open))
Z_Port_4	Capacitance	0.6 pF	true	Discrete	Custom	140 values ([ 0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
Z_Port_4	RealInductance	9.9 nH	true	Discrete	Custom	150 values ([ 0.1 nH : 10 nH : 0.1 nH], [11 nH : 60 nH : 1 nH])
Z_Port_4	Selector	RealL	true	Discrete	Auto	All values (C, RealL)
			false		Auto	All values (Enabled, Disabled (Short), Disabled (Open))
Z_Port_5	Capacitance	5.1 pF	true	Discrete	Custom	140 values ([ 0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
Z_Port_5	RealInductance	2.9 nH	true	Discrete	Custom	150 values ([ 0.1 nH : 10 nH : 0.1 nH], [11 nH : 60 nH : 1 nH])
Z_Port_5	Selector	RealL.	true	Discrete	Auto	All values (C, RealL)
Z_Port_16	EnabledState	Enabled	false	Discrete	Auto	All values (Enabled, Disabled (Short), Disabled (Open))
Z_Port_16	Capacitance	9 pF	true	Discrete	Custom	140 values ([ 0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
Z_Port_16	RealInductance	33 nH	true	Discrete	Custom	150 values ([ 0.1 nH : 10 nH : 0.1 nH], [11 nH : 60 nH : 1 nH])
Z_Port_16	Selector	RealL.	true	Discrete	Auto	All values (C, RealL)
XF-1	ActiveDataName	XF Result	false	Discrete	Auto	All values (XF Result)
GND	EnabledState	Enabled	false	Discrete	Auto	All values (Enabled, Disabled (Open))
Z_Port_25	Capacitance	9.5 pF	true	Discrete	Custom	140 values ([ 0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
Z_Port_25	RealInductance	3.4 nH	true	Discrete	Custom	150 values ([ 0.1 nH : 10 nH : 0.1 nH], [11 nH : 60 nH : 1 nH])
Z_Port_25	Selector	RealL	true	Discrete	Auto	All values (C, RealL)
Z_Port_24	Capacitance	14 pF	true	Discrete	Custom	140 values ([ 0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
Z_Port_24	RealInductance	3.3 nH	true	Discrete	Custom	150 values ([ 0.1 nH : 10 nH : 0.1 nH], [11 nH : 60 nH : 1 nH])
Z_Port_24	Selector	RealL	true	Discrete	Auto	All values (C, RealL)
V1						
V1						
Z Port 19	Capacitance	9.6 pF	true	Discrete	Custom	140 values ([0.1 pF : 10 pF : 0.1 pF ], [11 pF : 50 pF : 1 pF])
7 Port 19	RealInductance	60 pH	true	Discrete	Custom	$150 \times 10000 (101 \text{ pt} + 101 \text{ pt} + 01 \text{ pt} + 10 \text{ pt})$
7 Port 19	Selector	Reall	true	Discrete	Auto	
Generic Tuner Reta	FnahladState	Frabled	falso	Discrete	Auto	All values (Contract Displaced Open))
Generic Tuner Beta	CL	3.0 pE	true	Discrete	Auto	The modes (choice), because (open) All values (0.1 p. 6.0.2 p. 6.0.2 p. 6.0 p. 6.0 5 p. 6.6 p. 6.7 p. 6.8 p. 6.0 p. 6.1 p. 6.1 p. 6.1 2 p. 6.1 3 p. 6.1 4 p. 6.1 5 p. 7.0 p. 6.2 5 p. 6.3 p.
Generic Tuner Beta	0	0.1 pF	true	Discrete	Auto	
Generic Tuner Beta	SW1	OFF	true	Discrete	Auto	All values (OFE ON)
Generic Tuner Beta	SW2	OFF	true	Discrete	Auto	All values (OFF_ON)
Congric Tunger Bota	SW2	OFF	true	Discrete	Auto	All values (OFE ON)
Conorio Tunos Bota	SWO	OFF	true	Discrete	Auto	
Conoris Tunor Pota	SWA	01	true	Discrete	Auto	All roles (OE ; ON)
Generic Tuner beca	SWO	Con Con	oue	Discrete	Auto	All volumes (Verify, Verify)
GND 4						

Select the definition(s) to edit



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## **Design Goals**

 $\tilde{\bar{\uparrow}} \tilde{\bar{\uparrow}} XFdtd$  - Design Goals

### List of design goals

Goal Repor Pass/Fail / 5 ( 0.00 %) / 5 ( 0.00 %) / 4 ( 60.00 %)
/ 5 ( 0.00 %) / 5 ( 0.00 %) / 4 ( 60.00 %)
/ 5 ( 0.00 %) / 4 ( 60.00 %)
/ 4 ( 60.00 %)
/ 5 ( 0.00 %)
/ 5 ( 0.00 %)
/ 2 ( 60.00 %)
/ 3 ( 40.00 %)

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### **Optimized Fixed Components**



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